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PTO/SB/21 (09-04)

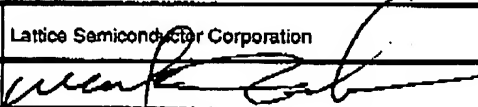
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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	10/685,920	
	Filing Date	9/18/2003	
	First Named Inventor	Edward E Miller	
	Art Unit	2816	
	Examiner Name	Hiep Nguyen	
Total Number of Pages in This Submission	<b>7</b>	Attorney Docket Number	L03-024

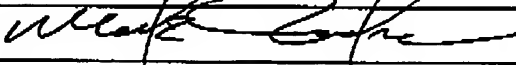
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**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm Name	Lattice Semiconductor Corporation		
Signature			
Printed name	Mark L. Becker		
Date	7/18/05	Reg. No.	91325

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**JUL 18 2005**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

First Named Inventor: Edward E Miller

Application No.: 10/665,920

Filed: 9/18/2003

For: CONTROLLED HYSTERESIS  
COMPARATOR WITH RAIL-TO-RAIL  
INPUT

Examiner: Hiep Nguyen

Art Unit: 2816

Attorney Docket No.: L03-024

**RESPONSE AFTER FINAL REJECTION**

Applicant responds to the Office action mailed 07/13/2005 as follows.